



0 323 030
A2

②

Int. Cl. G06F 11/10

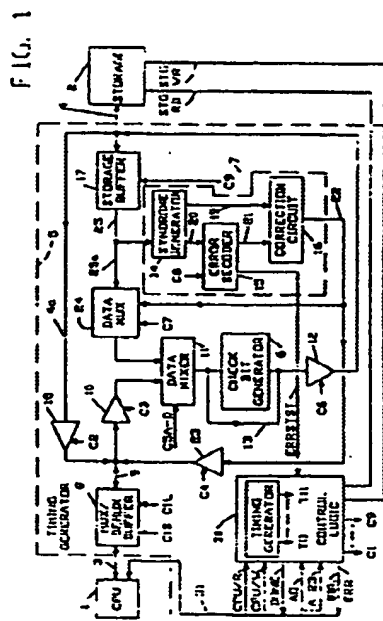
② Date of filing: 24.11.88

⑦ Applicant International Business Machines Corporation
Old Orchard Road
Armonk, N.Y. 10504(US)

(72) Inventor: Concha, Fernando
9604 Saddlebrook Drive
Boca Raton Florida(US)
Inventor: Stancil, Charles Jay
6710 Canary Palm Circle
Boca Raton Florida 33433(US)

20. Representative: Hawkins, Anthony George Frederick
IBM United Kingdom Limited Intellectual
Property Department Hursley Park
Winchester Hampshire SO21 2JN(GB)

⑦ In a data processing system using a read-modified-write process, data from a store is modified by data from a CPU and the modified data returned to the store. The data from the store includes error detecting and correcting bits, and, substantially simultaneously with a modifying operation, the data from the store is checked. If an error is detected, the modified data is not returned to the store, and the checked data is corrected and the corrected data is then modified by the data from the CPU and returned to the store.



EP 0 323 030 A2